

ICPU

Version 4.2.0 Announcement

Inspect-CPU Systems, Inc and ESAi are proud to announce ICPU version 4.2.0. This new version is yet another step in our ongoing commitment to customer success and our continuing effort to provide an even better solution to application performance problems in CICS®.

The highlight of this new release is complete TCB switching information which in addition to the current CPU usage information provides a complete performance picture of the application programs (see a screen example at the bottom).

ICPU now identifies the programs that are the best candidates for threadsafe conversion opportunities. As more programs become threadsafe enabled, work is offloaded from the QR TCB to an Open TCB. This results in reduced contention for the QR TCB thus increasing throughput as more work will be done in parallel. Further by helping to identify and prioritize candidates for conversion, ICPU lets you make best use of staff resources for most impact.

ICPU version 4.2.0 is compatible with all the previous versions and any inspections taken with previous versions will be displayed and printed as before.

ICPU is the only sampling product that automatically identifies the program and the executed code sequence that is causing **CPU spikes** resulting in erratic response times.

ICPU is the only sampling product that can run for an **unlimited time** on your production systems without any impact, presenting CPU savings opportunities even for well-performing applications.

ICPU is the only sampling product that integrates itself easily with the **application life-cycle** and can be used by **every** CICS professional. ICPU can be used freely by programmers during development time to assure well-tuned programs, and ICPU can be used in production to discover application performance problems as they occur.

The earlier performance problems are discovered, the less it costs to fix them. A system with well-tuned applications is more stable, causes fewer service interruptions and prolongs the lifetime of your site's hardware.

ICPU version 4.2.0 extends the diversified functionality of ICPU, which includes the following features:

1. **Real-time operation** - results are available at any time and you don't have to stop the sampling to get them.
2. **User-friendly** - easy to use; meaningful, easy-to-implement results.
3. **Data collection** - the sampling results are saved in memory buffers eliminating I/O overhead.
4. **Unlimited sampling** - any number of users at the same time, in any environment, for any length of time without affecting the overhead.
5. **Unattended operation** – starting sampling automatically when CICS starts up or at any predefined time.
6. **Source listing** - matching of sampling data directly to COBOL source code which is supported for COBOL compiler output generated from XPED, Intertest, Debug Tool and ENDEVOR.
7. **Check Point** - assuring that sampling results are not lost if CICS crashes.
8. **Standard coding** - ICPU uses only standard z/OS and CICS interfaces.
9. **Online help** - is available from any screen.
10. **Filtering** - by user/transaction/terminal/program.

11. **Batch reports** - can be produced at anytime, even when sampling is still going on.
12. **Model inspections Ids** - any previous inspection can be used as a model for a new one.
13. **Generic Inspection Ids** - shorthand for assigning and referring to inspections.
14. **Install and use** – A non-smpe installation procedure which can be performed in less than an hour with the same install materials for any CICS supported version from CICSTS 1.1 up to and including CICSTS 5.3, and any z/OS[®] version up to and including Z/OS 2.2.
15. **Installation verification programs** - distributed in source and load formats and can be used to verify ICPU installation and teach the user about the CPU usage of various CICS commands.

```

CICSTEST                                ICPU                                MAP0070
Command: ___                            Inspection Results Summary
                                           All TCBs
Userid: ICPU   Inspection Id: DEMO   Run Time: 03:13:25   Slice size: 0032
                                           Start: 12/29/14 07:12:27   Issued: 12/29/14 07:12:27
Status: Active End: 12/29/14 15:12:27 Sample Interval: 010   Repeat: N 007

Inspection Filters:
Userids:                                Tranids:
Pgmids:                                Termids:

```

DB2: 85.9 AS: 4.2 TS: 3.9 SP: 2.8 SPL: 2.7 PC: 1.8 FC: .6 SC: .6 OT: .2

Program Name	Lang	Hits	Appl	CPU% CICS	Osrv	Reqs	QR CMDS	No of TCB	No of SW Slices	Slice-CPU% (01)
NATAPP01	NAT	46,651	4.3	15.2	4.7	218,772	22,946	36,705	020	2010 - 12.3
NATNUC01	ASM	26,602	2.8	7.9	3.1	66,059	12,105	16,221	020	002AF460- 16.4
TST20000	COB	17,927	2.1	4.3	2.9	19,123	3,817	3,204	010	00000460- 62.3
TST30000	COB	11,759	1.8	2.3	2.0	8,368	8,368	0	003	00004C58- 12.1
====all====		192,771	29.7	43.9	26.4	486,716	46,702	60,474		

KEYS: ENTER=Refresh, 2=SW TCB, 4=BWD, 5=FWD, 7=Prev, 8=Next, 10=Left, 11=Right

ICPU is a powerful yet a simple tool. ICPU is the only APM solution for CICS that can be used effectively in any environment, for any length of time and the **information supplied by ICPU is not available otherwise.**

For more information, contact us at:

Enterprise Systems Associates, Inc. ("ESAI")
 UCF Research Park
 3259 Progress Drive
 Orlando, FL 32826
 Toll Free 866-464-3724

Email: info@ESAIGroup.com Web: www.ESAIGroup.com / www.inspect-cpu.com

© COPYRIGHT Inspect-CPU Systems, Inc.